APR 0 2 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Shunpei YAMAZAKI et al.

Serial No. 09/916,484

Filed: July 30, 2001

For: ELECTRIC DEVICE, MATRIX

DEVICE, ELECTRO-OPTICAL DISPLAY)

DEVICE, AND SEMICONDUCTOR

MEMORY HAVING THIN-FILM

TRANSISTORS

Art Unit: 2823

Examiner: S. Foong

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with The United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents,

Washington, D.C. 2023 I, on 3.28.2003 Udule M. Stamper

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INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner of Patents Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing references known to Applicant and requests that these references be made of record in the above identified application. Copies of the references listed are submitted herewith in accordance with 37 C.F.R. 1.98(a).

In accordance with 37 C.F.R. § 1.97(e), it is certified that either (1) each item of information contained in the Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement, or (2) no item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign patent application and no item of information contained in the Information Disclosure Statement was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this Information Disclosure Statement.

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Respectfully submitted,

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Signature

Approved for use through 10 31/2002. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

				Group Art Unit Examiner Name	APR 0 2 2003 5	2823 S. Foo	ong		
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